IN THE CLAIMS

Please amend the claims as follows.

1.   (Currently Amended)   For use in a data processor, a floating point unit comprising:

a plurality of floating point processing units capable of executing floating point instructions that write operands to an external memory and capable of executing floating point instructions that read operands from said external memory; and

an operand queue capable of storing a plurality of operands associated with one or more operations being processed in said floating point unit, wherein said operand queue stores a first operand written by a floating point write instruction executed by a first one of said plurality of floating point processing units and wherein said operand queue supplies said first operand to a floating point read instruction executed by a second one of said plurality of floating point processing units when said floating point read instruction requires said first operand, wherein said first operand is written <u>from said operand queue</u> to a buffer for storage in an external memory, and wherein a second operand is written directly to the buffer bypassing the operand queue.

2.   (Previously Presented)   The floating point unit as set forth in Claim 1 wherein said floating point unit further comprises a store conversion unit capable of converting operands in said plurality of floating point processing units from an internal format associated with said plurality of floating point processing units to an external format associated with the external memory.

3.   (Previously Presented)   The floating point unit as set forth in Claim 2 wherein said operand queue receives said first operand from said store conversion unit and transfers said first operand to said buffer for storage in said external memory.

4.   (Previously Presented)   The floating point unit as set forth in Claim 1 wherein said floating point unit further comprises a load conversion unit capable of converting incoming operands received from said external memory from an external format associated with an external memory to an internal format associated with said plurality of floating point processing units.

5.   (Original)   The floating point unit as set forth in Claim 4 wherein said operand queue receives said incoming operands from said external memory and transfers said incoming operands to said load conversion unit.

6.   (Original)   The floating point unit as set forth in Claim 5 wherein data in said external memory is accessed in groups of N bytes and wherein said floating point unit further comprises at least one aligner capable of receiving a first incoming operand that is misaligned with respect to a boundary between a first N byte group and a second N byte group and aligning said first incoming operand.

7.   (Original)   The floating point unit as set forth in Claim 6 wherein said operand queue receives said aligned first incoming operand from said at least one aligner.

8.   (Original)   The floating point unit as set forth in Claim 7 wherein said at least one aligner sets at least one bit in said operand queue to indicate that said aligned first incoming operand is valid.

9.   (Currently Amended)   A data processor comprising:

at least one pipelined integer execution unit;

a data cache;

an instruction cache; and

a floating point unit comprising:

a plurality of floating point processing units capable of executing floating point instructions that write operands to an external memory and capable of executing floating point instructions that read operands from said external memory; and

an operand queue capable of storing a plurality of operands associated with one or more operations being processed in said floating point unit, wherein said operand queue stores a first operand written by a floating point write instruction executed by a first one of said plurality of floating point processing units and wherein said operand queue supplies said first operand to a floating point read instruction executed by a second one of said plurality of floating point processing units when said floating point read instruction requires said first operand, wherein said first operand is written <u>from said operand queue</u> to a buffer for storage in an external memory, and wherein a second operand is written directly to the buffer bypassing the operand queue.

10.   (Previously Presented)   The data processor as set forth in Claim 9 wherein said floating point unit further comprises a store conversion unit capable of converting operands in said plurality of floating point processing units from an internal format associated with said plurality of floating point processing units to an external format associated with the external memory.

11.   (Previously Presented)   The data processor as set forth in Claim 10 wherein said operand queue receives said first operand from said store conversion unit and transfers said first operand to said buffer for storage in said external memory.

12.   (Previously Presented)   The data processor as set forth in Claim 9 wherein said floating point unit further comprises a load conversion unit capable of converting incoming operands received from said external memory from an external format associated with an external memory to an internal format associated with said plurality of floating point processing units.

13.   (Original)   The data processor as set forth in Claim 12 wherein said operand queue receives said incoming operands from said external memory and transfers said incoming operands to said load conversion unit.

14. (Original)   The data processor as set forth in Claim 13 wherein data in said external memory is accessed in groups of N bytes and wherein said floating point unit further comprises at least one aligner capable of receiving a first incoming operand that is misaligned with respect to a boundary between a first N byte group and a second N byte group and aligning said first incoming operand.

15. (Original)   The data processor as set forth in Claim 14 wherein said operand queue receives said aligned first incoming operand from said at least one aligner.

16. (Original)   The data processor as set forth in Claim 15 wherein said at least one aligner sets at least one bit in said operand queue to indicate that said aligned first incoming operand is valid.

17.   (Currently Amended)   For use in a floating point unit comprising a plurality of floating point processing units capable of executing floating point instructions that write operands to an external memory and capable of executing floating point instructions that read operands from the external memory, a method of accessing the operands comprising:

storing in an operand queue a first operand written by a floating point write instruction executed by a first one of the plurality of floating point processing units;

supplying the first operand from the operand queue to a floating point read instruction executed by a second one of the plurality of floating point processing units when the floating point read instruction requires the first operand;

writing the first operand from the operand queue to a buffer for storage in the external memory; and

writing a second operand directly to the buffer bypassing the operand queue.

18.   (Previously Presented)   The method as set forth in Claim 17 wherein the floating point unit further comprises a store conversion unit capable of converting operands in the plurality of floating point processing units from an internal format associated with the plurality of floating point processing units to an external format associated with the external memory.

19.   (Previously Presented)   The method as set forth in Claim 18 further including:

storing the first operand from the store conversion unit into the operand queue.

20.   (Previously Presented)   The method as set forth in Claim 17 wherein the floating point unit further comprises a load conversion unit capable of converting incoming operands received from an external memory from an external format associated with the external memory to an internal format associated with the plurality of floating point processing units.

21.   (Previously Presented)   The method as set forth in Claim 20 further including:

storing the incoming operands from the external memory in the operand queue; and

transferring the incoming operands from the operand queue to the load conversion unit.